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Diagram illustrating the layout of a floating-point number (likely IEEE 754) with a 32-bit mantissa and a 16-bit mantissa, separated by an alias label.

Top row (32-bit mantissa): 1 sign bit, 8 exponent bits, 23 mantissa bits.

Bottom row (16-bit mantissa): 1 sign bit, 5 exponent bits, 10 mantissa bits.

The label "(Alias)" is positioned between the two rows, indicating that the 16-bit format is an alias for the 32-bit format.

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